

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	<b>Application Number</b>		10776926	
	<b>Filing Date</b>		2004-02-11	
	<b>First Named Inventor</b>	Takahiro Yamaguchi		
	<b>Art Unit</b>	2611		
	<b>Examiner Name</b>	E. Bayard		
<b>Attorney Docket Number</b>		02008/092002		

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1	Office Action dated October 14, 2008 issued by the German Patent & Trademark Office in corresponding Application No. 2959526, 12 pages	<input checked="" type="checkbox"/>
2	Yamaguchi, T.J., et al, "Extraction of Peak-to-Peak and RMS Sinusoidal Jitter Using an Analytic Signal Method" 18th IEEE VLSI Test Symposium 2000, Montreal 4/30/2000, 8 pages	<input type="checkbox"/>
3	Miller, C.M., et al., "Jitter Analysis of High-Speed Digital Systems" Hewlett-Packard Journal, Vol. 46, February 1995, 8 pages	<input type="checkbox"/>
4	Baldini, J.J., et al., "Jitter in Digital Transmission Systems - Characteristics and Measure Techniques, IEEE Global Telecommunication Conference, Miami, New York: IEEE, Vol. 23 CONF., November 1982, 7 pages	<input type="checkbox"/>

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